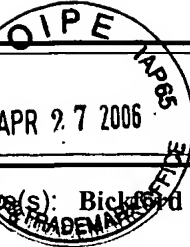


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C of O

**TRANSMITTAL LETTER**
(General - Patent Issued)Docket No.
BUR920030117US1

Patent(s): Bickford et al.

U.S. Patent No.

7,013,441 **62**

Issue Date

March 14, 2006

Title: **METHOD FOR MODELING INTEGRATED CIRCUIT YIELD**COMMISSIONER FOR PATENTS:

Transmitted herewith is:

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Dated: 04-24-2006

Jack P. Friedman, Ph.D.
Reg. No. 44,688
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(518) 220-1850I certify that this document and fee is being deposited on **March 24, 2006** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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P17A/REV03

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**PATENT NO : 7,013,441 **B2**

DATED : March 14, 2006

INVENTOR(S) : Bickford et al.

It is certified that error appears in the above-identified patent and that said Letters Patent
is hereby corrected as shown below:

Column 1

Line 65, delete "sign"

MAILING ADDRESS OF SENDER: Jack P. Friedman, Ph.D.

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PATENT NO. 7,013,441

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